

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A liquid crystal display device comprising:  
a pair of substrates;  
a liquid crystal layer filled between said pair of substrates; and  
a plurality of pixel electrodes and common electrodes , both of said pixel electrodes and said common electrodes being supported on one of said pair of substrates, for supplying an electric field to said liquid crystal layer, said electric field having a component which extends substantially in parallel to said one of said pair of substrates;

wherein the liquid crystal display device is configured so that a response time between a lowest brightness level and a highest brightness level is less than 16.7 ms; and

wherein said liquid crystal layer contains a range of ~~from 40% or more weight percentage to less than 100% or less weight~~ percentage of a constituent component with a dielectric anisotropy of  $|\Delta\epsilon| \leq 1$ .

2. (currently amended) A liquid crystal display device comprising:  
a pair of substrates;  
a liquid crystal layer filled between said pair of substrates; and  
a plurality of pixel electrodes and common electrodes , both of said pixel electrodes and said common electrodes being supported on one of said pair of

substrates, for supplying an electric field to said liquid crystal layer, said electric field having a component which extends substantially in parallel to said one of said pair of substrates;

wherein the liquid crystal display device is configured so that a response time between a lowest brightness level and a highest brightness level is less than 16.7 ms; and

wherein said liquid crystal layer contains a range of ~~from~~ 40% or more weight percentage to no more than 90% or less weight percentage of a constituent component with a dielectric anisotropy of  $|\Delta\epsilon| \leq 1$ .

3. (currently amended) A liquid crystal display device comprising:  
a pair of substrates;  
a liquid crystal layer disposed between said pair of substrates; and  
a plurality of pixel electrodes and common electrodes, both of said pixel electrodes and said common electrodes being supported on one of said pair of substrates, for supplying an electric field to said liquid crystal layer, said electric field having a component which extends substantially in parallel to said one of said pair of substrates;

wherein the liquid crystal display device is configured so that response time between gray levels is less than 16.7 ms; and

wherein said liquid crystal layer contains a range of ~~from~~ 40% or more weight percentage to less than 100% or less weight percentage of a constituent component with a dielectric anisotropy of  $|\Delta\epsilon| \leq 1$ .

4. (currently amended) A liquid crystal display device comprising:

a pair of substrates;  
a liquid crystal layer disposed between said pair of substrates; and  
a plurality of pixel electrodes and common electrodes , both of said pixel electrodes and said common electrodes being supported on one of said pair of substrates, for supplying an electric field to said liquid crystal layer, said electric field having a component which extends substantially in parallel to said one of said pair of substrates;

wherein the liquid crystal display device is configured so that response time between gray levels is less than 16.7 ms; and

wherein said liquid crystal layer contains a range of ~~from~~ 40% ~~or more weight to no more than~~ 90% ~~or less weight~~ percentage of a constituent component with a dielectric anisotropy of  $|\Delta\epsilon| \leq 1$ .

5. (currently amended) A liquid crystal display device comprising:  
a pair of substrates;  
a liquid crystal layer disposed between said pair of substrates; and  
a plurality of pixel electrodes and common electrodes , both of said pixel electrodes and said common electrodes being supported on one of said pair of substrates, for supplying an electric field to said liquid crystal layer, said electric field having a component which extends substantially in parallel to said one of said pair of substrates;

wherein said liquid crystal layer has a birefringence  $\Delta n$  and a dielectric anisotropy  $\Delta\epsilon$  which satisfy the condition  $\Delta n/\sqrt{\Delta\epsilon} \leq 5.5 \times 10^{-2}$ ; and

wherein said liquid crystal layer contains a range of from 40% or more weight percentage to less than 100% or less weight percentage of a constituent component with a dielectric anisotropy of  $|\Delta\epsilon| \leq 1$ .

6. (original) A liquid crystal display device according to claim 5, wherein a distance L between said pixel electrodes and said common electrodes satisfies the condition of  $L \times \Delta n / \sqrt{\Delta\epsilon} \leq 0.55 \mu\text{m}$ .

7. (original) A liquid crystal display device according to claim 5, wherein a distance L between said pixel electrodes and said common electrodes satisfies the condition of  $L \times \Delta n / \sqrt{\Delta\epsilon} \leq 0.4 \mu\text{m}$ .

8. (previously presented) A liquid crystal display device according to claim 1, wherein said pixel electrodes and said common electrodes are provided in different layers which are supported on said one of said pair of substrates.

9. (previously presented) A liquid crystal display device according to claim 2, wherein said pixel electrodes and said common electrodes are provided in different layers which are supported on said one of said pair of substrates.

10. (previously presented) A liquid crystal display device according to claim 3, wherein said pixel electrodes and said common electrodes are provided in different layers which are supported on said one of said pair of substrates.

11. (previously presented) A liquid crystal display device according to claim 4, wherein said pixel electrodes and said common electrodes are provided in different layers which are supported on said one of said pair of substrates.

12. (previously presented) A liquid crystal display device according to claim 5, wherein said pixel electrodes and said common electrodes are provided in different layers which are supported on said one of said pair of substrates.

13. (previously presented) A liquid crystal display device according to claim 8, wherein said pixel electrodes and said common electrodes are arranged in a substantially nonoverlapping relation in the different layers.

14. (previously presented) A liquid crystal display device according to claim 9, wherein said pixel electrodes and said common electrodes are arranged in a substantially nonoverlapping relation in the different layers.

15. (previously presented) A liquid crystal display device according to claim 10, wherein said pixel electrodes and said common electrodes are arranged in a substantially nonoverlapping relation in the different layers.

16. (previously presented) A liquid crystal display device according to claim 11, wherein said pixel electrodes and said common electrodes are arranged in a substantially nonoverlapping relation in the different layers.

17. (previously presented) A liquid crystal display device according to claim 12, wherein said pixel electrodes and said common electrodes are arranged in a substantially nonoverlapping relation in the different layers.

18. (new) A liquid crystal display device according to claim 1, wherein said  $|\Delta\epsilon| > 0$ .

19. (new) A liquid crystal display device according to claim 2, wherein said  $|\Delta\epsilon| > 0$ .

20. (new) A liquid crystal display device according to claim 3, wherein said  $|\Delta\epsilon| > 0$ .

21. (new) A liquid crystal display device according to claim 4, wherein said  $|\Delta\epsilon| > 0$ .

22. (new) A liquid crystal display device according to claim 5, wherein said  $|\Delta\epsilon| > 0$ .